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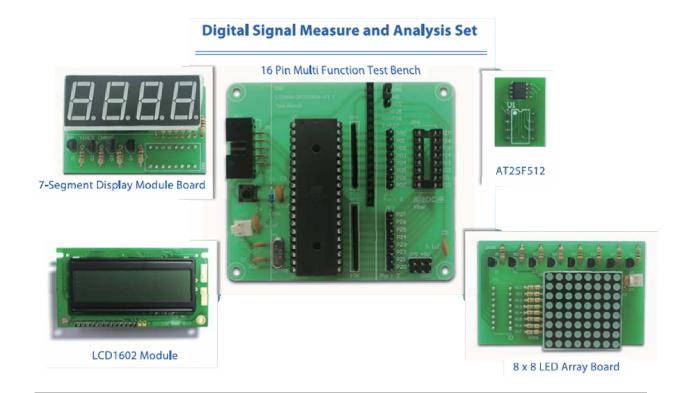
中和區建八路 123 號 2 樓 2F., No.123, Jian 8th Rd., 電話:886-2-66202225 傳真: 886-2-22234362 www.zeroplus.com.tw

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## **Digital Signal Measure and Analysis Set**

This product is co-designed by ZEROPLUS and a high school electronic department teacher. Product package contents the following five boards composed of simulation development environment, but also authorized an open learning materials for all sample code, can be used as an auxiliary teaching aids, practical exercises control flexibly embedded peripheral modules, suitable for all digital circuitry design entry learners adopt!

ZEROPLUS has been working with the academic aspects of education with close cooperation, but also joined sponsorship of many MCU thematic project contest. New launched <Digital Signal Measure and Analysis Set>, is ideal for college students who do a variety of topics in signal when simulation board, and then with ZEROPLUS logic analyzer decodes and analysis the digital signal, we can let the original cumbersome time-consuming and expensive workers debug program easier to understand, and also allows students to enter the workforce as soon as possible to establish the competitiveness!



Based on AT89S51, the test circuit board can directly program the firmware in the mode of ISP, so the operation is convenient. The LCD port on the test circuit board can test the LCD module; the 16 Pin multifunctional test port can test the basic digital IC and all kinds of EEPROMs and Flashes, easy to test and track the digital signal % find the detail spec. :<click here>

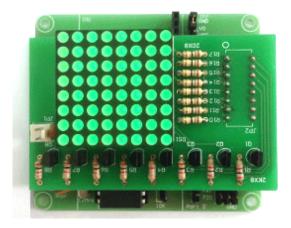


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Learning with "Zeroplus Logic Analyzer (LAP-C 16032 above)" Easy & Quickly build up digital design/debug competence through practice !



Modules on work ~